UNIMORE
UNIVERSITÀ DEGLI STUDI DI
MODENA E REGGIO EMILIA

Lunedì 16 Maggio 2022, ore 14.00-15.30
c/o edificio MO 25, Aula P1.3

Dr. Paolo Toniutti, Infineon Technologies Villach
terrà un seminario dal titolo

"CMOS devices for Industrial and Automotive applications: Design for high-yield manufacturing"

nell'ambito del corso di Laurea Magistrale in Electronic Engineering
e del Corso di Dottorato di Ricerca in ICT

Tutti gli interessati sono invitati a partecipare

Abstract: Modern analog/mixed-signal applications require ultra-scaled CMOS technologies in order to fulfill demanding computation requirements. The fabrication of such technologies has to ensure stringent technical and yield specifications for MOSFETs, resistors, capacitors and BJT devices. In this context, state-of-the-art modelling, simulation and design are key to achieve high-yield production. Device reliability is also fundamental to guarantee the required performance until the end of the product lifetime. The seminar will address all these topics with special emphasis on the needs of dependable electronics for modern automotive applications

Paolo Toniutti received the Bachelor and Master Degree in Electronic Engineering at the University of Udine, Italy in 2005 and 2008, respectively. In 2012, he received the Double Degree Ph.D. from the University of Udine, Italy and the University of Grenoble, France. During his Ph.D. he has been working on modeling and simulation with Monte Carlo techniques to analyze carrier transport in ultra-scaled CMOS technologies.

Since 2012 he joined Infineon Technologies Austria, Villach, where he is now working as Design-Technology-Interface Engineer on the modeling and simulation of physical mechanisms concerning the electrical behavior and the reliability of CMOS devices for analog/mixed-signal applications.